

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4745	156/345.11 156/345.22 156/345.31 156/345.32 156/345.54 134/61 134/63 134/902 134/184 134/133 438/906	US-PGPUB; USPAT	OR	OFF	2006/02/03 15:21
L2	281	1 and (door lid shutter cover) with (computer control\$4)	US-PGPUB; USPAT	OR	OFF	2006/02/03 17:11
L3	1032	1 and (rins\$3 same dry\$3 same (clean\$3 wash\$3 etch\$3))	US-PGPUB; USPAT	OR	OFF	2006/02/03 15:54
L4	990	3 and water	US-PGPUB; USPAT	OR	OFF	2006/02/03 15:56
L5	905	4 not 2	US-PGPUB; USPAT	OR	OFF	2006/02/03 15:56
L6	243	5 and (control\$4 actuat\$3) with (lid cover open\$3 door gate\$1)	US-PGPUB; USPAT	OR	OFF	2006/02/03 16:26
L7	32066	(wafer semiconductor substrate) and (control\$4 actuat\$3) with (lid cover open\$3 door gate\$1)	EPO; JPO; DERWENT	OR	OFF	2006/02/03 16:30
L8	0	7 and b65g4902.ipc.	EPO; JPO; DERWENT	OR	OFF	2006/02/03 16:27
L9	15283	(wafer semiconductor substrate) with (control\$4 actuat\$3) with (lid cover open\$3 door gate\$1)	EPO; JPO; DERWENT	OR	OFF	2006/02/03 16:31
L10	0	b65g0490002.ipc.	EPO; JPO; DERWENT	OR	OFF	2006/02/03 16:32
L11	65	9 and b65g049\$.ipc.	EPO; JPO; DERWENT	OR	OFF	2006/02/03 16:40
L12	357	9 and g02f001\$.ipc.	EPO; JPO; DERWENT	OR	OFF	2006/02/03 16:46
L13	6173	9 and h01l021\$.ipc.	EPO; JPO; DERWENT	OR	OFF	2006/02/03 16:56
L14	4368	13 not (gate with electrode)	EPO; JPO; DERWENT	OR	OFF	2006/02/03 16:57
L15	297	(11 12) not (gate with electrode)	EPO; JPO; DERWENT	OR	OFF	2006/02/03 17:07
L16	4576	14 15	EPO; JPO; DERWENT	OR	OFF	2006/02/03 16:57
L17	23	16 and (plural\$3 multiple) with (chamber\$1 vessel\$1)	EPO; JPO; DERWENT	OR	OFF	2006/02/03 16:58
L18	15	maegawa-tadashi.in. hiroe-toshio. in.	EPO; JPO; DERWENT	OR	OFF	2006/02/03 17:08
L19	8	maegawa-tadashi.in. hiroe-toshio. in.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/03 17:10

L20	2303	dainippon.as.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/02/03 17:10
L21	68	20 and (door lid shutter cover) with (computer control\$4)	US-PGPUB; USPAT	OR	OFF	2006/02/03 17:11